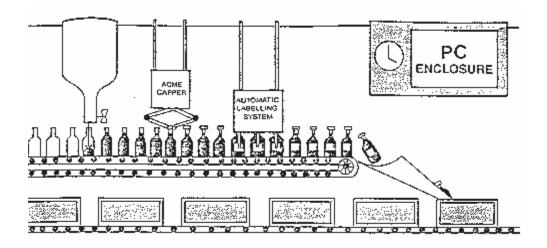
Unit 1, Overview of PLCs

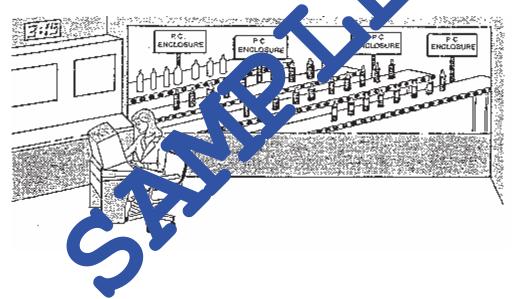
SPY

Learning Steps

Student Action	Where	Media	
View	Unit 1, Introduction to PLCs	The PLCTrainer Tutorial	
Complete	Unit 1, Introduction to PLCs	The PLCTrainer Lab	
		Manual	
Stop	Unit 1, Introduction to PLCs	Have Instructor Evaluate	
	Assignment Sheet 1	Assignment Sheet 1	
Complete	Unit 1, Introduction to PLCs	The PLCTrainer Lab	
	Assignment Sheet 2	Manual	
Stop	Unit 1, Introduction to PLCs	Have Instructor Evaluate	
	Assignment Sheet 2	Assignment Sheet 2	
Complete	Unit 1, Practical Exercise 1	Unit 1, Practical Exercise 1	
		Alanual	
Stop		Vav Instructor Evaluate	
		CTrainer Lab	
		In Jal Practical Exercise 1	
Complete	Unit 1, Practical Corcise	Tave Instructor Evaluate	
		The PLCTrainer Lab	
		Manual Practical Exercise 2	
Review	Unit 1, In Jour on to ZCs	The PLCTrainer Tutorial	
See Instructor			



The high-speed demand and the computer age have together caused the evolution of a faster more precise process control system, the Programmable Logic Controller. This graphic shows the bottling process being more efficiently of a Programmable Logic Controller. The PLC offers greater flexibility and faster that has been the old relay control systems.



As technology continues to expand, so will our methods of automated control. Today it is possible to link many control systems together under the management of a central control system.

Functions of Programmable Controllers

PLCs make system control much more efficient than the electro-mechanical systems of the past. Additionally, PLC's are capable of performing many functions that the older systems just could not do. These might include:

Practical Exercise 2

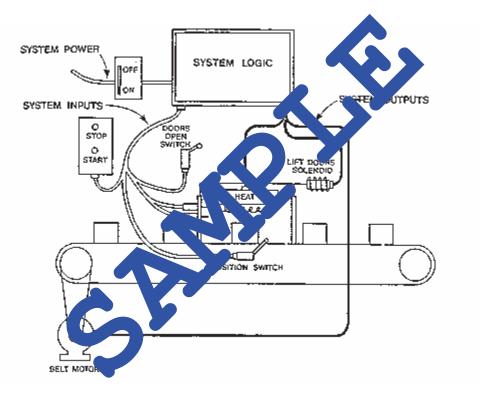
Directions:

Match the following input or output Devices with the statement that best explains their function in the system using your logical ability to determine what device causes what action. You will not find answers to this question within the tutorial.

When the system is started, the product moves into the 'oven'. There it stops and the doors will close. Once the target temperature is achieved for a specific period, the doors will open and the product moves out as another moves in place.

- Α. Temperature Sensors C.
- Β. Position Switch
- Doors open switch
- E. Heating Element

- D. Start button
- F. **Drive Motor**



- 1. De-activates, signaling the Logic to begin The heating.
- The will signal the Logic to start the belt moving. 2.
- The will determine whether the heat is at the proper 3. level.

If you are New To PLCs then Please Read This.!

LogixPro allows you to practice and develop your RSLogix programming skills where and when you want. It replaces the PLC, ladder rung editor, and all the electrical components that have until now, been required to learn RSLogix. It doesn't however; replace instructors, texts, tutorials or PLC documentation manuals etc. which are so essential when learning about PLCs and RSLogix. Think of LogixPro as a Word Processor which will allow you to practice your literary skills **after** you are familiar with the meaning of words and how they are spelled

The LogixPro Screen

The most commonly used elements of LogixPro are displayed below. The **Edit Panel** provides easy access to all the RSLogix instructions and they may be simply dragged and dropped into your program.

Once your program is ready for testing, clicking on the "Toggle Button" of the Edit Panel will bring the **PLC Panel** into view. From the PLC Panel you can download your program to the "PLC" and then place it into the "RUN" mode. This will initiate the scanning of your program and the I/O of your chosen simulation.



Editing Your Prog

If you're familiar with Windows and how to use a mouse, then you are going to find editing a breeze. Both Instructions and Rungs are selected simply by clicking on them with the left mouse button. Deleting is then just a matter of hitting the Del key on your keyboard.

Double Clicking (2 quick clicks) with the left mouse button allows you to edit an instruction's address while right clicking (right mouse button) displays a pop-up menu of related editing commands.

Click on an Instruction or Rung with the left mouse button and keep it held down and you will be able to drag it wherever you please. Let go of it on any of the tiny locating boxes that you will see, and the Instruction or Rung will cling to its new home. Isn't Windows Grand!

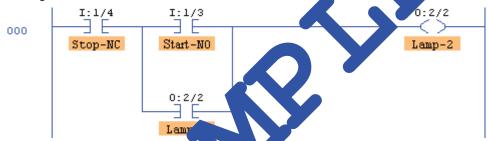


Activate the Start and Stop switches and ensure that the OTL and OTU output instructions are responding as outlined in your text. Once you have the lamp ON, could you turn it off if power was lost in the Stop Switch circuit?

Now modify your program so that it operates correctly when you substitute the N.O. Stop switch (I:1/03) with a Normally Closed Switch. If we now lost power on the N.C. Stop switch circuit, what would happen to the state of Lamp (O:2/02)?

Emulating Standard Stop/Start Control

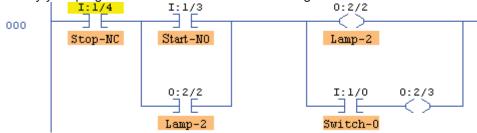
Erase your program by selecting "New" from the "File" menu selection at the top of the screen. When the dialog box appears just click on "OK" to select the prate PLC type. Now enter the following program. To enter a branch, just drag the branch (but to be rung and then insert or drag instructions into the branch.



Before you download and run this param, take a careful look at our use of a XIC instruction to test the state of the N.C. n someone presses the Stop Switch, will bit I:1/04 go Switch. o True or False when the Switch is pressed? Is this the True or False? Will the ารเ Run the program and see if you're right! If we loose logic we are seeking in this what state will the lamp go to? Why do you think that most power in the Stor ch cird d rath e OTL/OTU method of implementing Stop/Start Control? prefer this meth than

Output Branching with RSLogix

Modify your program so that it matches the following.



Download and Run the program. Operate the Stop and Start switches several times with Switch-0 open, and again with Switch-0 closed. Remove the XIC instruction from the Output branch and

• Run the timers through their timing sequence a number of times. Don't move on until you are satisfied that the timers are working as you would expect

In this exercise we have utilized just two timers, but there is nothing stopping us from sequencing as many timers as we wish. The only thing to remember is; to use the DN (done) bit of the previous timer to enable the next timer in the sequence. Obviously locating the timers on consecutive rungs, and employing consecutive numbering will make such a program much easier to read and trouble-shoot.

Self Resetting Timers

• Place the PLC into the PGM mode, and modify the first rung of your program as depicted below.



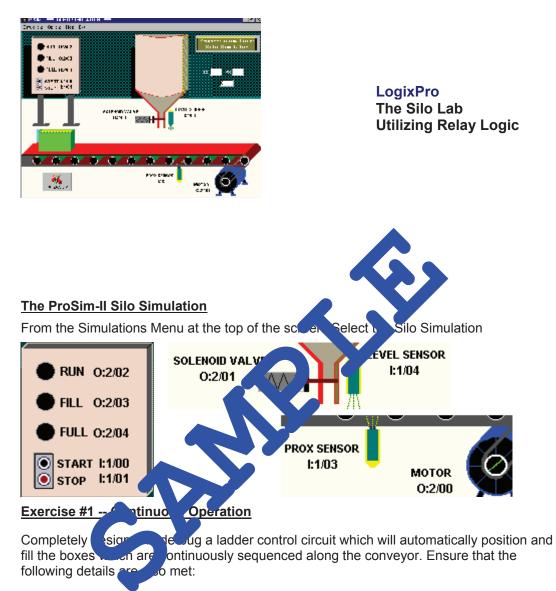
- Once you have modified your program, download it to the PLC and place the PLC into the RUN mode.
- Close switch I:1/0 and observe the operation of tim s. The timers should now be operating in a continuous loop with ner2, and then when Timer2 is starting As before, when Timer1 is reset, it in turn done, Timer1 is reset by Timer2's ne bi do to go low (T4:2/DN=0). Once Timer2's done resets Timer2 which causes Tim It ginally began, and the timing sequence will bit is low, the sequence is bad whe start over once again on the ve ct so
- Remove the first instruction is 1:1/0) from rung zero of your program.
- Download and P modify version of your program
- Does the tiging operation antinuously sequence as before? It should!
- Can you stop the time a sequence? Not without taking the PLC out of the RUN mode! In many approximations here may never be a need to stop such a timing sequence, so a switch might not be used or needed.

In this exercise we cascaded two timers, but as before there is nothing to stop us from cascading as many timers as we wish. The thing to remember here is; utilize the DN (XIC or "NOT" done) bit of the last timer in the sequence to reset the first timer in the sequence. Once again, consecutive rungs, and numbering will make a program much easier to read and trouble-shoot.

The TOF Timer (Timer OFF Delay)

In Allen Bradley PLC programming, the TON timer is by far the most commonly used type of timer. Most people consider TON timers to be simple to use and understand. In comparison, many people find the operation of the Allen Bradley TOF (Timer OFF delay) timer to be less intuitive, but I'm going to let you decide for yourself.

Scenario 1:



- The sequence can be stopped and re-started at any time using the panel mounted Stop and Start switches.
- The RUN light will remain energized as long as the system is operating automatically.
- The RUN light, Conveyor Motor and Solenoid will de-energize whenever the system is halted via the STOP switch.

Assignment Sheet 1:

- 1. The terms table or file are used when referring to:
 - \Box (A) Consecutive groups of words
- □ (C) A large metal cabinet

□ (B) Two words

- $\hfill\square$ (D) Both A and C
- 2. The earlier Get and Put instructions were replaced with the MOV instruction:
 - □ True

□ False

3. The MOV instruction will:

 \Box (A) Copy from a source word. data.

□ (B) Write to a destination

□ (D) Both A and B.

(C) Move alphanumeric

4. Data compare instruction. To not include a Not Equal To instruction:

brd

□ True

□ False.

5. Which of the following is not a valid Data Compare instruction?

□ <>	. =
□ <=	□ >=<

Scenario 1:

Design, program and test and document the following control system using Math Functions. (Use the I/O Simulator with LogixPro)

KWT Automation has secured the contract to alter the bottling line at a local sparkling water company. You are assigned the task of writing a program that will operate the system. The graphic below shows the layout of the line. A sensor has been added at the beginning of the line to count the bottles entering the line. There is also a sensor counting the bottles completing the process. The bottling operation should maintain a 94% success rate to meet projected profit margins. If the rate falls below 94% an alarm must sound to alert maintenance personnel. If it falls below 90% the alarm must sound and the line must be shut down.

The system must operate effectively no matter how many bottles have been processed. Remember, at any given time there are 10 bottles in the system. Also be careful that a division by zero does not occur while you are calculating the percentage. A division by zero will usually cause a CPU fault.

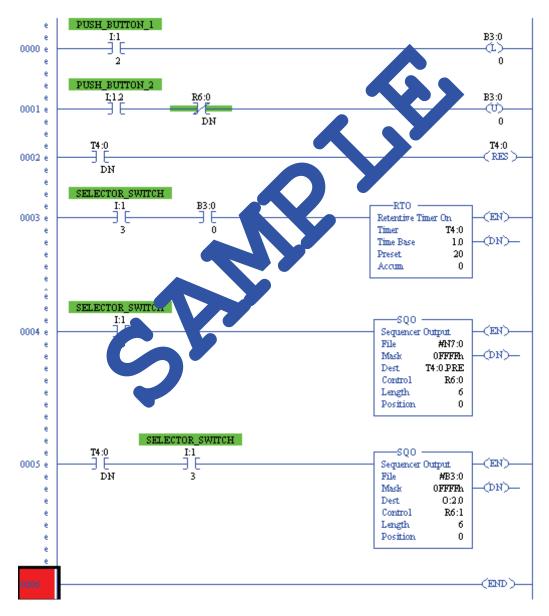
NOTE: The F8 files are not functional in LogixPro, the fore of data must be converted to whole numbers, and the equivalent values of the used.

Practical Exercise:

This exercise will utilize a Master and Slave PLC. You must be running a DH485 network with at least 2 nodes in order to complete this exercise.

The program on the slave PLC will contain two sequencer instructions, SQO:0 and SQO:1. It will also contain one RTO instruction. As shown in the diagram below, the T4:0/DN bit will be used to forward the sequencers to the next step.

Each sequencer has six steps. The second sequencer will be called the Preset-Control Sequencer. It will be used for regulating the delay between steps of the first sequencer, the Event-Control Sequencer. The Preset-Control Sequencer transfers time delay information to the T4:0 Preset Register. The DN bit of timer T4:0 is used to reset the RTO.



Introduction:

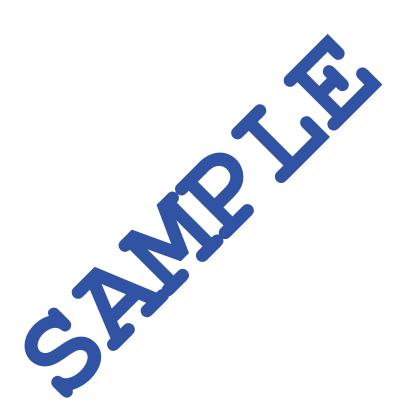
HMIs are an essential part of many PLC managed processes. The competent technician will understand the uses and operation of Human Machine Interfaces.

Terminal Objective:

Upon completion of this unit the learner will be able to discuss HMIs.

Enabling Objectives:

- 1. Describe the use of HMIs.
- 2. Discuss HMI graphics.



Assignment Sheet 1:

- 1. What are the things that all effective troubleshooting has in common?
 - □ Symptom recognition. □ Problem isolation.
 - □ Corrective action. □ All of the above.
- 2. When troubleshooting a PLC managed system, it is never necessary to question the operator.
 - □ True
 - □ False
- 3. If this LED is not on, the DC power supply s not function
 - □ Processor Fault.
 - DC Power On .
- 4. If this status light is on the within the processor.
 - □ Processor Faul
 - DC Power On.
- 5. This status light we be on when a parity error exists between the memory module and the processor module;

🗆 I/O

Battery Low.

□ I/O Fault.

□ Battery Low.

- □ Memory Fault. □ I/O Fault.
- □ DC Power On. □ Processor Fault.

Appendix A

Status File Overview

The status file lets you monitor how your operating system works and lets you direct how you want it to work. This is done by using the status file to set up interrupts, load memory module programs, and monitor both hardware and software faults.

Note: Do not write to status file data unless the word or bit is listed as dynamic/static configuration in the descriptions that follow. If you intend writing to status file data, it is imperative that you first understand the function fully. The status file S: contains the following words:

Word	Function applies to all processors	
S:0	Arithmetic Flags	
S:1	Processor Mode Status/Conc	
S:2	STI Bits / DH485 mms	
S:3L	Curre Last can Ti	
S:3H	V shdo, car Time	
S:4	Tre Rung Clock	
S.	Error Bits	
S:6	vlajor Error Code	
7,5	Suspend Code/Suspend File	
s,S:10	Active Nodes (DH-485)	
S:11,S:12	I/O Slot Enables	
S:13,S:14	Math Register	
S:15L	Node Address	
S:15H	Baud Rate	

Word	Function applies to SLC 5/02, SLC 5/03,SLC 5/04 processors

address of Bit file 3 is contained in address N10:2. Therefore, if N10:2 contains the value 5, B3:[N10:2] indirectly refers to address B3:5. Other examples include:

• N7:[N7:0], N[N7:0]:[N7:1], N7[T4:0.ACC], C5:[N7:0]

Indexed Indirect Addressing

- You may specify a combination of indirect and indexed addressing. The processor first resolves the indirect portion of the address and then adds the offset from the Index Register S:24 to come up with the final address. For example:
- #N7:[N10:3] where N10:3= 20 and S:24 =15 The actual address used by the instruction is N7:35.

Troubleshooting Faults

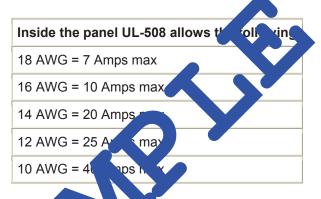
The major error fault code, indicates the probable causes of s, d recommends corrective action. The following section describes the difference was to automatically clear a fault using your programming software.

Automatically Clearing Faults: SLC Proc con

- Set the Fault Override at Power to sit S⁺ p in the status file to clear the fault when power is cycled, assuming the set p, am is not corrupt.
- Set one of the auto load 24, 0, 11, or S:1/12 in the status file of the program in an EEPROM to automatic. Turk ster a new non-faulted program from the memory module to RAM when power correled.
- Refer to Status Bits and information on bits S:1/13, S:1/8, S:1/10, S:1/11, S:1/12, S:5/0-7, a. .36/0-. You can declare your own application specific major fault by writing our own big value to S:6 and then setting hit S:1/I3.

Unspecified voltages (relay contact for customer connections)	Gray
---	------

Voltages DC	Color
24v DC(+)	Brown
24v DC(-)	Blue
DC grounded circuits	White / Blue stripe
Foreign Voltage (Outside the panel)	Yellow
Temporary Jumpers	Purple (easy to see)
Oddball voltages (Speed Control potentiometer wiring)	Violet
Unspecified voltages (relay contact for customer connections)	Gray

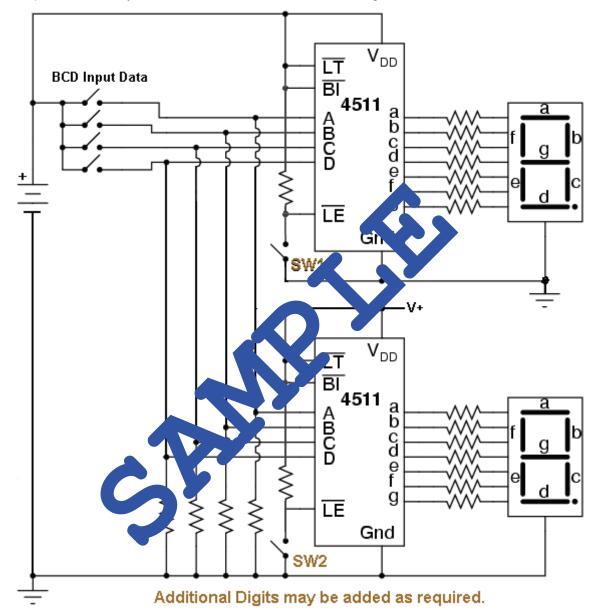


The allowable Current is Lower and the stated values when the wire is in conduit, wireway or ductbank. Go to the NEC for these requirements.

- Most places use 14 W no mum for 120 VAC field wiring, regardless of amps.
- Typical **16 AWG m** hum for 24 VDC field control circuits, and **18 AWG shielded** twister pair **20 nA** wires (Belden 1032A or eq.)
- Avoid using 14 12 AWG shielded twisted pair for 4-20 mA wiring it adds nothing in function, cost, more, and is a pain to work with inside instrument enclosures.
- Do not allow wire nuts on 4-20 mA or other analog signal wires they get loose, they pick up EMI/RFI, and are just no good. Field splices should only be made at terminal strips in junction boxes!
- It's important to note any **"foreign voltage"** on the panel, since "foreign" wires may still be hot after the panel is powered down and there isn't really a standard.

Multiplexed Digits

By making use of the 4-bit latches that are built into the 4511 IC, we can easily multiplex the digits if so desired... By properly controlling the state of each latch enable pin (LE) we can use the same input data lines (4 switches) to selectively write to each 7-Segment display independently... With just a minor modification to our circuit, we will be able to essentially treat each digit as a unique 4-bit memory location where BCD data of our choosing can be stored and retained...



In the above schematic diagram, each display may be written to separately... First the BCD equivalent of the desired numeral (0-9) is set using the 4 data switches... If SW1 is then closed, the current BCD input data will enter the latch of the upper 4511 IC, and will be passed on to the decoder causing the numeral to displayed... if SW1 is then opened, the latch will retain the current data, but will now ignore any changes on it's inputs... The desired numeral will continue to be displayed by the upper LED display until power is lost, or SW1 is again closed and new data is allowed to enter it's latch...

The previous pages where just a sample of ...

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